

The diagram illustrates a Multicast Router architecture for video distribution. On the left, multiple input channels are shown, each consisting of a CAMERA (C1, C2, ..., Cn) connected to an ENCODER (E1, E2, ..., En), which is then connected to a PROCESSOR (P1, P2, ..., Pn). The output of each processor is fed into a central MULTICAST ROUTER. The router's output is connected to an IP NETWORK (N), which then distributes the content to multiple DISPLAYS (D1, D2, D3).

FIG. 1 MULTIPLE VIDEO ENCODERS

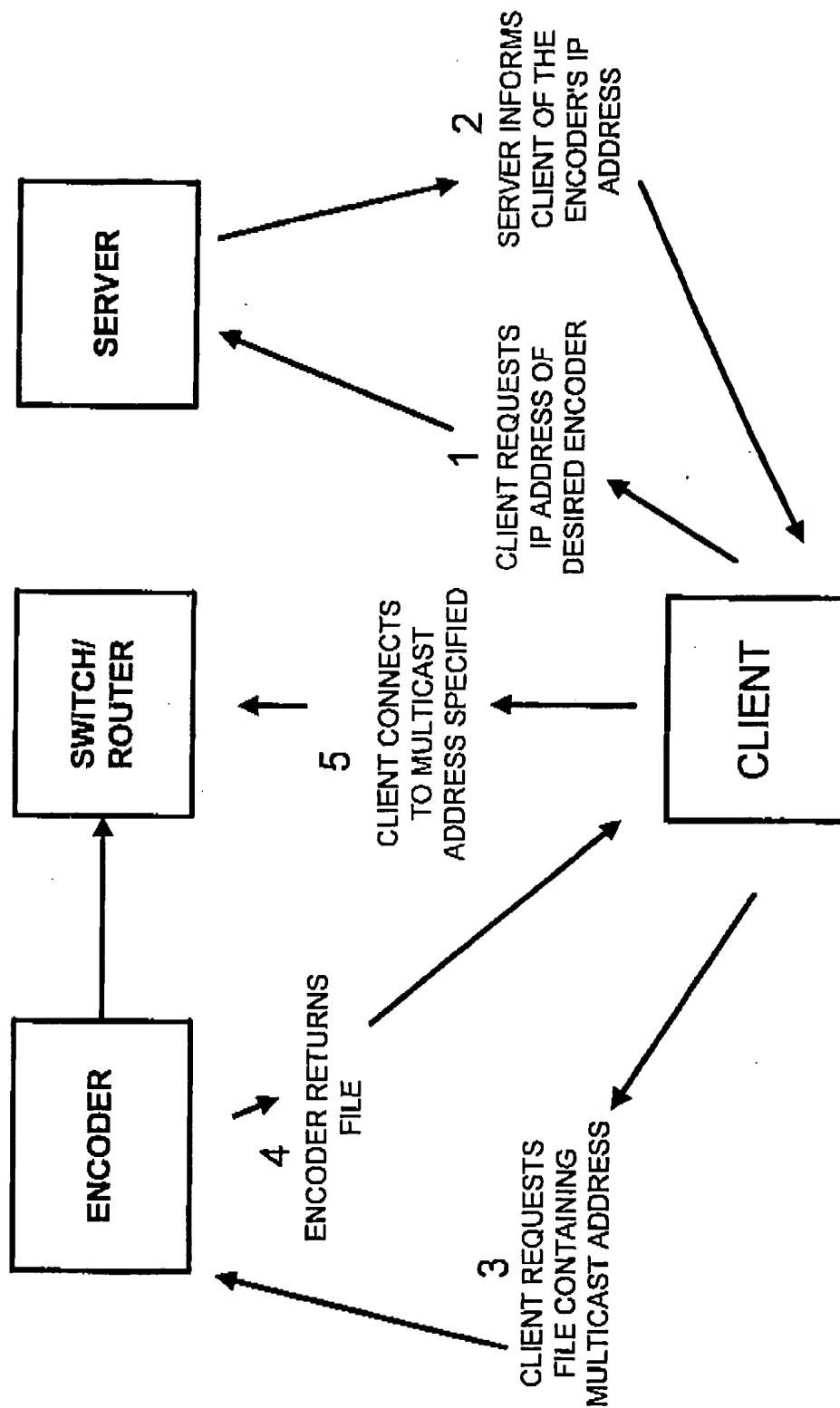


Fig. 2 MULTICAST ADDRESS RESOLUTION

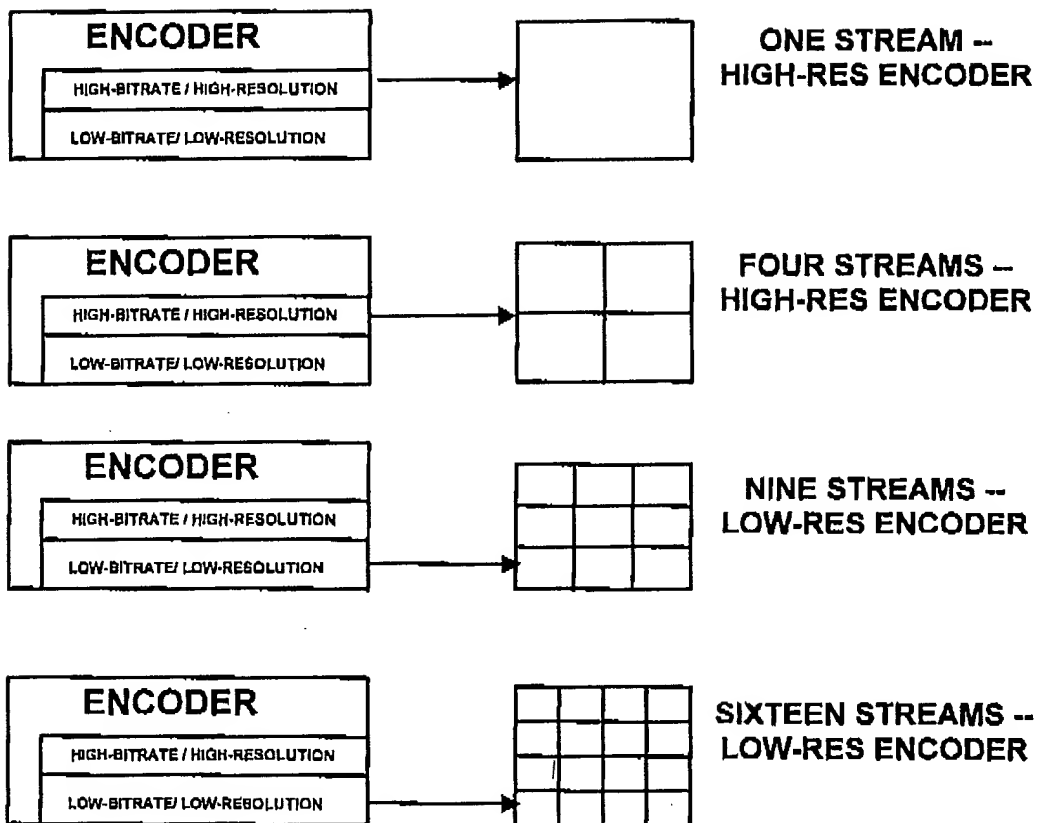


FIG. 4 -- BANDWIDTH CONSERVATION